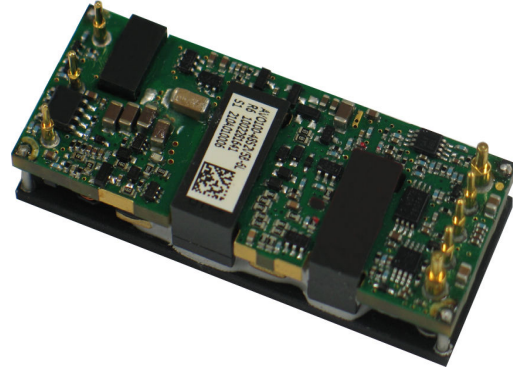


## Description

The AVO100-48S2V5B DC-DC converter is a next-generation industry standard eighth-brick with optimized ultra-high efficiency. This module can provide up to 35A output current at 2.5V output voltage, industry standard eighth-brick 57.9mm × 22.9mm × 12.7mm (2.28" × 0.9" × 0.5"), which makes it an ideal choice for small space, telecom and datacom applications. The AVO100-48S2V5B is standard eighth-brick pin-out configuration, with baseplate option. It provides CNT remote control, trim and sense functions, with OVP, OCP, OTP full protection method. This product can achieve ultra-high efficiency of 90.8% at full load. A heatsink can be installed for better thermal performance.



## Operational Features

- Up to 35A output current
- Ultra-high efficiency 90.8% typ. at full load
- Wide input range: 36V ~ 75V
- Excellent thermal performance
- No minimum load requirement
- RoHS 6 compliant

## Control Features

- Remote control function
- Remote output sense
- Trim function: 80% ~ 110%

## Protection Features

- Input under-voltage lockout
- Output over-current protection
- Output over-voltage protection
- Over-temperature protection

## Mechanical Features

- Industry standard eighth-brick
- Standard module with a baseplate
- Pin length option: 3.8mm

## Safety & EMC

- Meets safety standards UL 60950-1 2nd edition, IEC/EN 60950-1 2nd edition and GB4943
- Approved by UL and TUV
- Meets the Low Voltage Directives 2006/95/EEC with the Amendment Directive 93/68/EEC which facilitates CE marking in user's end product
- Materials meet UL94,V-0 flammability rating
- Meets conducted emission's requirements of FCC Class A and EN55022 Class A with external filter

## Electrical Characteristics

Full operating ambient temperature range is -40°C to +85°C.

Specifications are subject to change without notice. Test condition: Ta: 25°C. Air velocity: 300LFM.

Parameter		Min.	Typ.	Max.	Unit	Notes & conditions
<b>Absolute max. ratings</b>						
Input voltage	Non-operating	-	-	100	V	100ms
	Operating	-	-	80	V	Continuous
Operating temperature		-40	-	+85	°C	-
Storage temperature		-55	-	+125	°C	-
Voltage at remote ON/OFF pin		-0.7	-	12	V	-
<b>Input characteristics</b>						
Operating input voltage range		36	48	75	V	-
Input under-voltage lockout	Turn-on voltage threshold	31	-	36	V	-
	Turn-off voltage threshold	30	-	35	V	-
	Lockout voltage hysteresis	1	-	3	V	-
Max. input current		-	-	3.5	A	36V <sub>in</sub> , full load
No-load input current		-	0.05	-	A	
Standby input current		-	0.005	0.015	A	Remote OFF
Inrush current transient rating		-	-	1	A <sup>2</sup> s	Figure 15
Input reflected ripple current		-	-	40	mA	Through 12μH inductor; Figure 15
Recommended input fuse		-	-	10	A	External fast blow fuse is recommended; Figure 11
Recommended external input capacitance		100	-	-	μF	Low ESR capacitor is recommended; Figure 11
<b>Output characteristics</b>						
Output voltage set point (standard option)		2.46	2.5	2.54	V	48V <sub>in</sub> , half load
Output voltage line regulation		-	±0.1	±0.2	%	-
		-	-	-	mV	-
Output voltage load regulation		-	±0.1	±0.5	%	-
		-	-	-	mV	-
Output voltage temperature regulation		-	-	0.02	%/°C	-

Parameter		Min.	Typ.	Max.	Unit	Notes & conditions
Total output voltage range		2.41	2.5	2.59	V	Over sample, line, load, temperature & life
Output voltage ripple and noise		-	55	-	mVpp	Figure 2 20MHz bandwidth; Figure 15
Output voltage ripple and noise		-	70	-	mVpp	Ta: 25°C, Air velocity: 300LFM, Vin: 48V, Vonom, Ionom, 10u tantalum (ESR ≤ 100 mΩ)/1u ceramic capacitor, output ≤ 12V
Operating output current range		0	-	35	A	-
Output DC current-limit inception		37	-	50	A	Hiccup: auto-restart when over-current condition is removed
Output capacitance		330	-	10000	μF	High frequency and low ESR are recommended
<b>Dynamic characteristics</b>						
Dynamic response	25% ~ 50% ~ 25% Io,max, 0.1A/μs	-	30	-	mV	Figure 4 Test condition: 25°C, nominal input voltage, Figure 11
	Settling time	-	60	-	μs	Recovery to within 1% Vo,nom
	25% ~ 50% ~ 25% Io,max, 1A/μs	-	160	-	mV	Figure 5 Test condition: 25°C, nominal input voltage, Figure 11
	Settling time	-	50	-	μs	Recovery to within 1% Vo,nom
Turn-on transient	Rise time	-	5	50	ms	Full load, Figure 6
	Turn-on delay time	-	62	200	ms	-
	Output voltage overshoot	-	0	-	%Vo	-
<b>Efficiency</b>						
100% load		-	90.8	-	%	Figure 1
60% load		-	91.7	-	%	Figure 1
20% load		-	87.5	-	%	Figure 1

## Electrical Characteristics (Continued)

Parameter	Min.	Typ.	Max.	Unit	Notes & conditions	
<b>Isolation characteristics</b>						
Isolation voltage (conditions: 1mA for 60s, slew rate of 2000V/10s)	2250	-	-	V	Basic insulation, pollution degree 2, input to output	
	1500	-	-	V	Functional insulation, pollution degree 2, input to baseplate	
	750	-	-	V	Functional insulation, pollution degree 2, output to baseplate	
<b>Feature characteristics</b>						
Switching frequency	-	165	-	kHz	-	
Remote ON/OFF control (positive logic)	Off-state voltage	-0.7	-	1.2	V	Figure 12
	On-state voltage	3.5	-	12	V	
Remote ON/OFF control (negative logic)	Off-state voltage	3.5	-	12	V	
	On-state voltage	-0.7	-	1.2	V	
Output voltage trim range	2.0	-	2.75	V	See <i>Trim Characteristics of Application Note</i>	
Output voltage remote sense range	-	-	0.2	V	-	
Output over-voltage protection (dynamic)	2.95	-	4.0	V	Hiccup: auto-restart when over-voltage condition is removed	
Over-temperature shutdown	-	114	-	°C	Auto recovery; over-temperature protect (OTP) test point: Figure 10, Tested under thermal balance condition.	
Over-temperature hysteresis	-	-	-	°C	-	
<b>Reliability characteristics</b>						
Calculated MTBF (telcordia)	-	1.5	-	106h	300LFM, 40C Ta. Normal input/rated output@80%load	

## Electromagnetic Compatibility Characteristics

Test item	Regulations	Criteria	Notes & conditions
Conducted emission	EN 55022 DC input port, class A limits	/	See <i>EMC Test Conditions</i>
Immunity to electrostatic discharge	IEC/EN61000-4-2 Enclosure port, level 3	B	
Immunity to electrical fast transient	IEC/EN61000-4-4 DC input port, level 3	B	
Immunity to surges	IEC/EN61000-4-5 DC input port Line to ground (earth): 600V Line to line: 600V	B	
Immunity to continuous conducted interference	IEC/EN61000-4-6 DC input port, level 2	A	
Immunity to voltage dips and short interruptions and voltage variations	EN 61000-4-29 DC input port	B	

Criterion A: Normal performance during and after test.

Criterion B: For EFT and surges, low-voltage protection or reset is not allowed. Temporary output voltage fluctuation ceases after disturbances ceases, and from which the EUT recovers its normal performance automatically. For dips and ESD, output voltage fluctuation or reset is allowed during the test, but recovers to its normal performance automatically after the disturbance ceases.

## Qualification Testing

Parameter	Unit (pcs)	Test condition
Halt test	4 ~ 5	Ta, min-10°C to Ta, max+10°C, 5°C step, V <sub>in</sub> = min to max, 0 ~ 105% load
Vibration	3	Frequency range: 5Hz ~ 20Hz, 20Hz ~ 200Hz, A.S.D: 1.0m2/s3, -3db/oct, axes of vibration: X/Y/Z. Time: 30 min/axis
Mechanical shock	3	30g, 6ms, 3 axes, 6 directions, 3 time/direction
Thermal shock	3	-40°C to +100°C, unit temperature 20 cycles
Thermal cycling	3	-40°C to +55°C, temperature change rate: 1°C/min, cycles: 2 cycles
Humidity	3	40°C, 95%RH, 48h
Solder ability	15	IPC J-STD-002C-2007

# Characteristic Curves

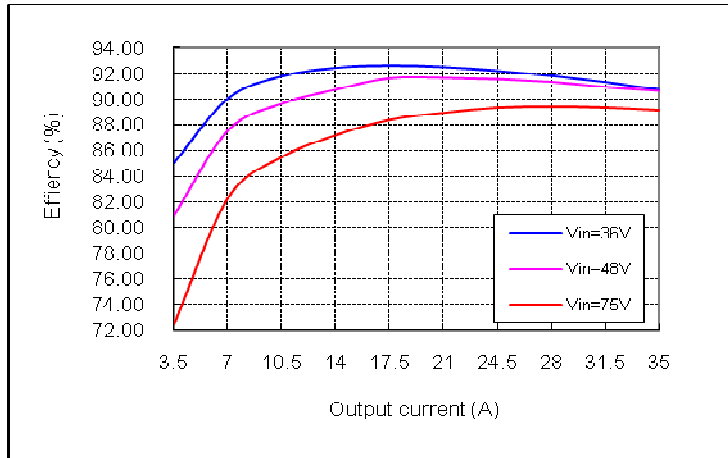


Figure 1 Efficiency vs. output current,  $T_a = 25^{\circ}\text{C}$ ,  $V_o = 2.5\text{V}$ , Air velocity = 300LFM

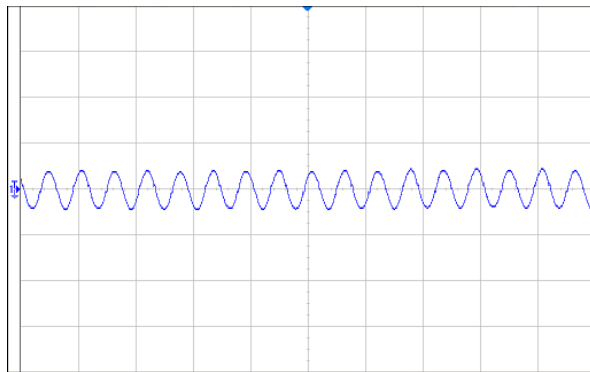


Figure 2 Output ripple & noise (5 $\mu\text{s}/\text{div}$ , 50mV/div), see Figure 15 for test configuration

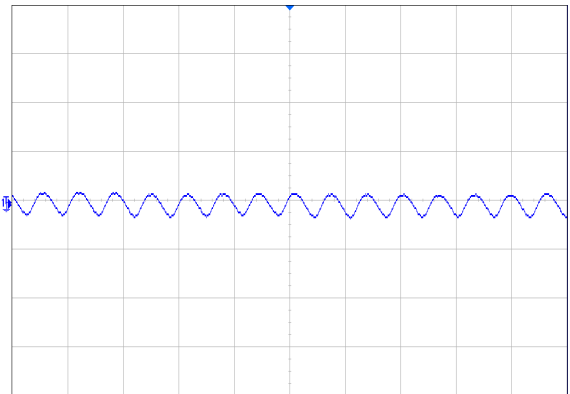


Figure 3 Input reflected ripple current (5 $\mu\text{s}/\text{div}$ , 10mA/div), see Figure 15 for test configuration

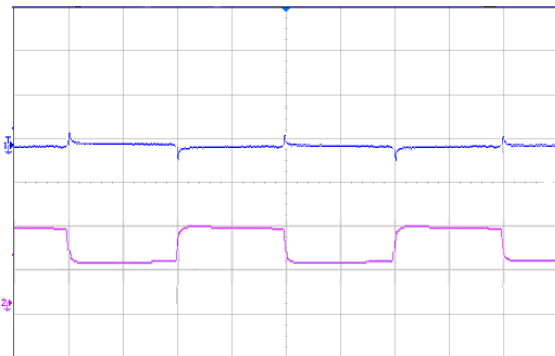


Figure 4 Dynamic response for 25% load step (25% ~ 50% ~ 25%) and 0.1A/ $\mu\text{s}$  slew rate, (2ms/div), see Figure 11 for test configuration; CH1-output voltage (50mV/div); CH2-output current (10A/div)

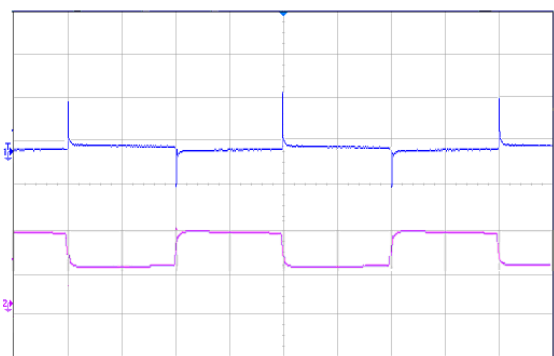


Figure 5 Dynamic response for 25% load step (25% ~ 50% ~ 25%) and 1A/ $\mu\text{s}$  slew rate, (2ms/div), see Figure 11 for test configuration; CH1-output voltage (50mV/div); CH2-output current (10A/div)

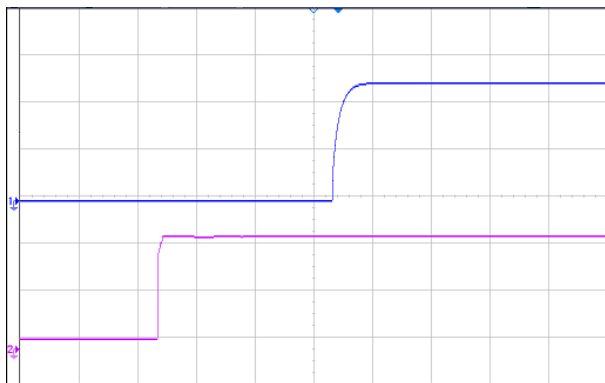


Figure 6 Output voltage startup by power-on, (50ms/div), see Figure 11 for test configuration; CH1-output voltage (1V/div); CH2-input voltage (20V/div)

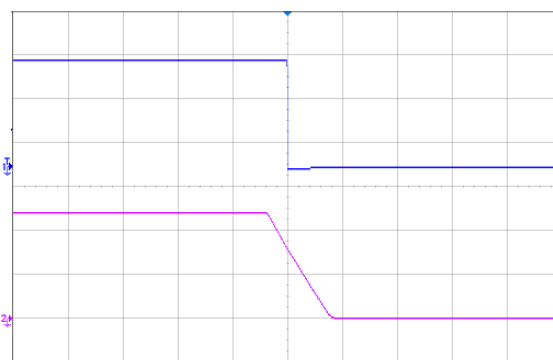


Figure 7 Output voltage shutdown by power-off, (100ms/div), see Figure 11 for test configuration; CH1-output voltage (1V/div); CH2-input voltage (20V/div)

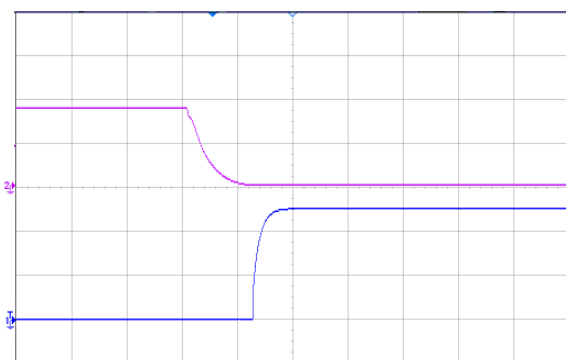


Figure 8 Output voltage startup by remote ON, (20ms/div), see Figure 11 for test configuration; CH1-output voltage (1V/div); CH2-remote ON voltage (2V/div) (Negative logic)

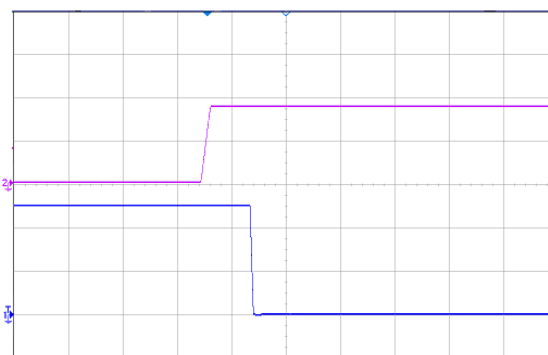


Figure 9 Output voltage shutdown by remote OFF, (20ms/div), see Figure 11 for test configuration; CH1-output voltage (1V/div); CH2-remote OFF voltage (2V/div) (Negative logic)

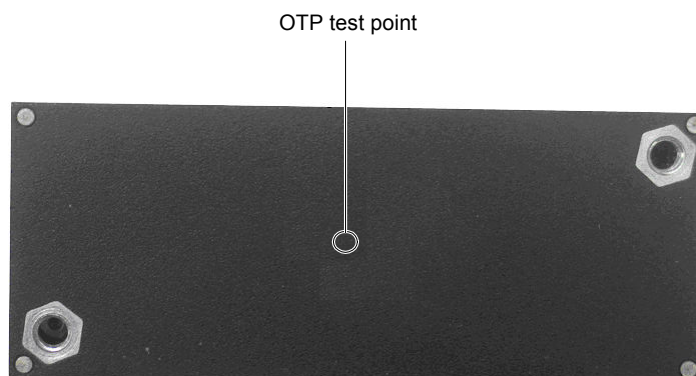


Figure 10 OTP test point (center of the baseplate)

# Application Note

## Typical Application

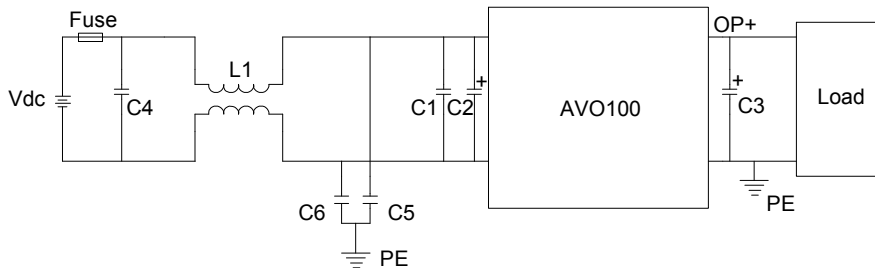


Figure 11 Typical application

Recommended input fuse: LITTLEFUSE 216010.P 10A

C1: SMD ceramic-100V-100nF-±10%-X7R-1206

C2: 100µF/100V electrolytic capacitor, high frequency and low ESR

C3: 1000µF/10V electrolytic capacitor, high frequency and low ESR

C4: SMD ceramic-100V-1000nF-X7R-1210

C5, C6: SMD ceramic-47nF/1000V/X7R-1210

L1: 1320uH-±25%-4A-R5K-21mm × 21mm × 12.5mm

## Remote ON/OFF

Either positive or negative remote ON/OFF logic is available in AVO100-48S2V5B. The logic is CMOS and TTL compatible.

The voltage between pin Remote ON/OFF and pin  $V_{in-}$  must not exceed the range listed in *Feature Characteristics* to ensure proper operation. The external remote ON/OFF circuit in AVO100-48S2V5B is highly recommended as shown in Figure 12.

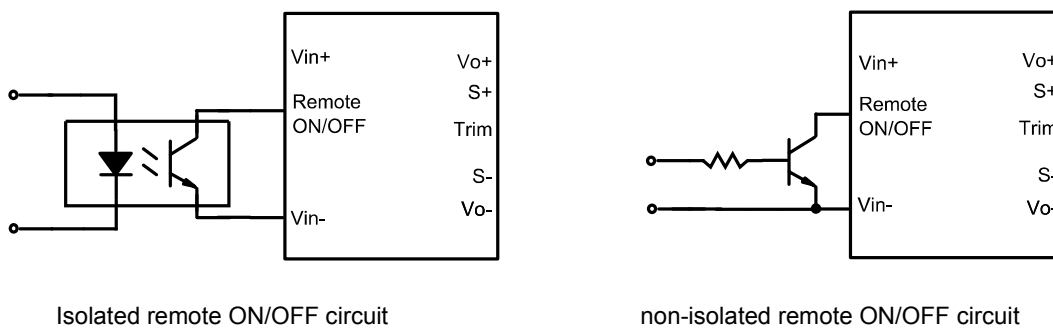


Figure 12 External remote ON/OFF circuit



### Trim Characteristics

Connecting an external resistor between Trim pin and V<sub>o-</sub> pin will decrease the output voltage, while connecting it between Trim and V<sub>o+</sub> will increase the output voltage. The following equations determine the external resistance to obtain the trimmed output voltage.

$$R_{adj\_down} = \left( \frac{511}{\Delta\%} - 10.22 \right) k\Omega$$

$$R_{adj\_up} = \left( \frac{5.11 V_{out} (100 + \Delta\%)}{V_{ref} \Delta\%} - \frac{511}{\Delta\%} - 10.22 \right) k\Omega$$

*R<sub>adj\_down</sub>* : Value of the external adjustment resistor which shall be connected between Trim and S- for trimming down.

*Δ%* : Output voltage change rate against nominal output voltage.

*R<sub>adj\_up</sub>* : Value of the external adjustment resistor which shall be connected between Trim and S+ for trimming up.

*V<sub>out</sub>*: Nominal output voltage.

*V<sub>ref</sub>* = 1.225V

When trimming up, the output current should be decreased accordingly so as not to exceed the maximum output power as shown in below figure.

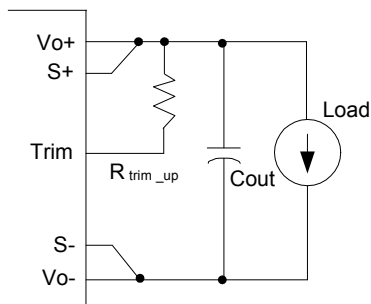


Figure 13 Trim up

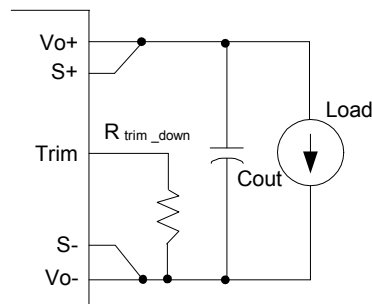


Figure 14 Trim down

### Sense Characteristics

If the load is far from the unit, connect S+ and S- to the terminals of the load respectively to compensate the voltage drop on the transmission line.

If the sense compensate function is not necessary, connect S+ to V<sub>o+</sub> and S- to V<sub>o-</sub> directly.

## Input Ripple & Inrush Current And Output Ripple & Noise Test Configuration

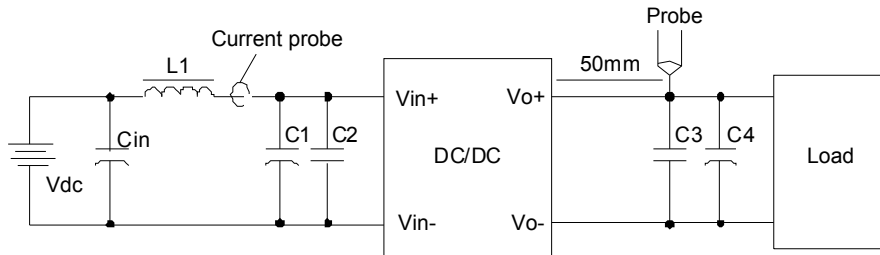


Figure 15 Input ripple & inrush current, output ripple & noise test configuration

Vdc: DC power supply

L1: 12 $\mu$ H

Cin: 220 $\mu$ F/100V typical

C1: SMD ceramic-100V-100nF $\pm$ 10%-X7R-1206

C2: 100 $\mu$ F/100V electrolytic capacitor, high frequency and low ESR

C3: SMD ceramic-10V-1 $\mu$ F $\pm$ 10%-X7R-1206

C4: 1000 $\mu$ F/10V electrolytic capacitor, high frequency and low ESR

Note: It is recommended to use a coaxial cable with series 50 $\Omega$  resistor and 0.68 $\mu$ F ceramic capacitor or a ground ring of probe to test output ripple & noise.

## EMC Test Conditions

See Figure11.

## Thermal Considerations

The converter is designed to operate in different thermal environments and sufficient cooling must be provided. Proper cooling of the DC-DC converter can be verified by measuring the temperature at the test points as shown in Figure 16 and Figure 17. The temperature at these points should not exceed the maximum values in Table 1 when the module is operating.

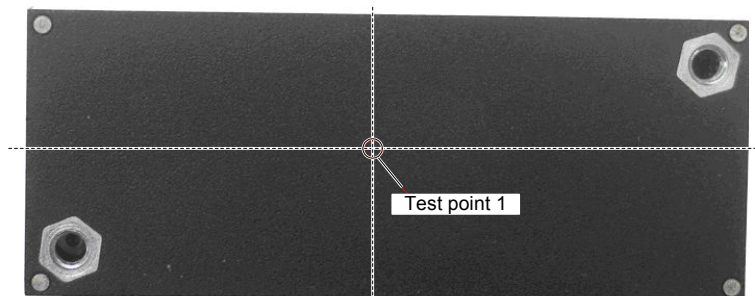


Figure 16 Thermal test point (top)

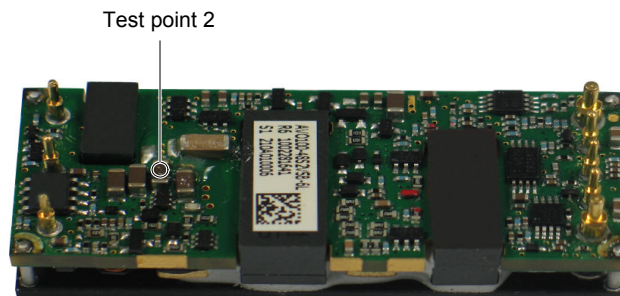


Figure 17 Thermal test point (bottom)

Table 1 Temperature limit of the test points

Test point	Temperature limit
Test point 1	112°C
Test point 2	118°C

The converter can operate with a smaller heatsink and sufficient airflow. For a typical application, forced airflow direction is from  $V_{in-}$  to  $V_{in+}$ . Figure 19 shows the derating of output current vs. ambient air temperature at different air velocity with a specified heatsink. The heatsink specification is shown in Figure18.

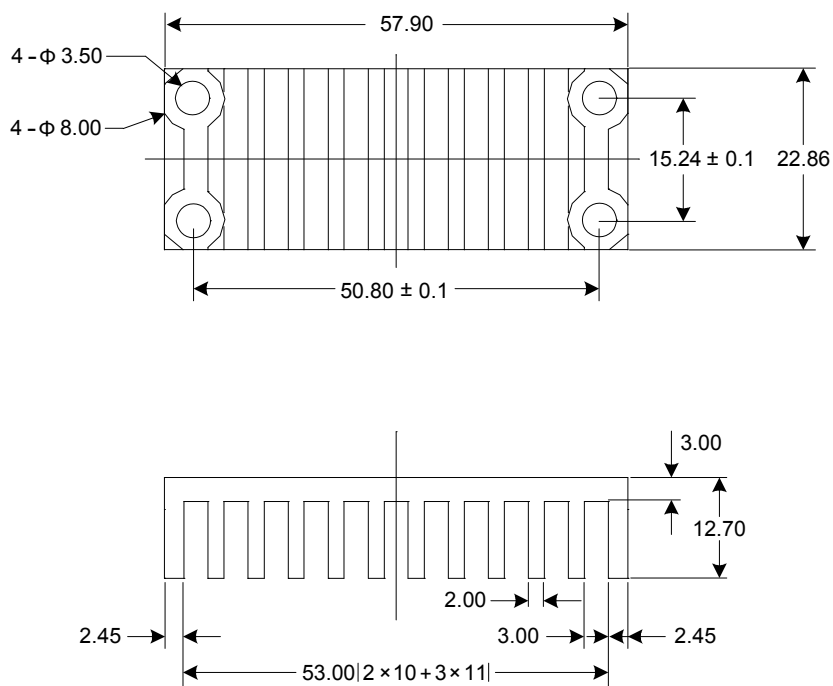


Figure 18 Heatsink (unit:mm)

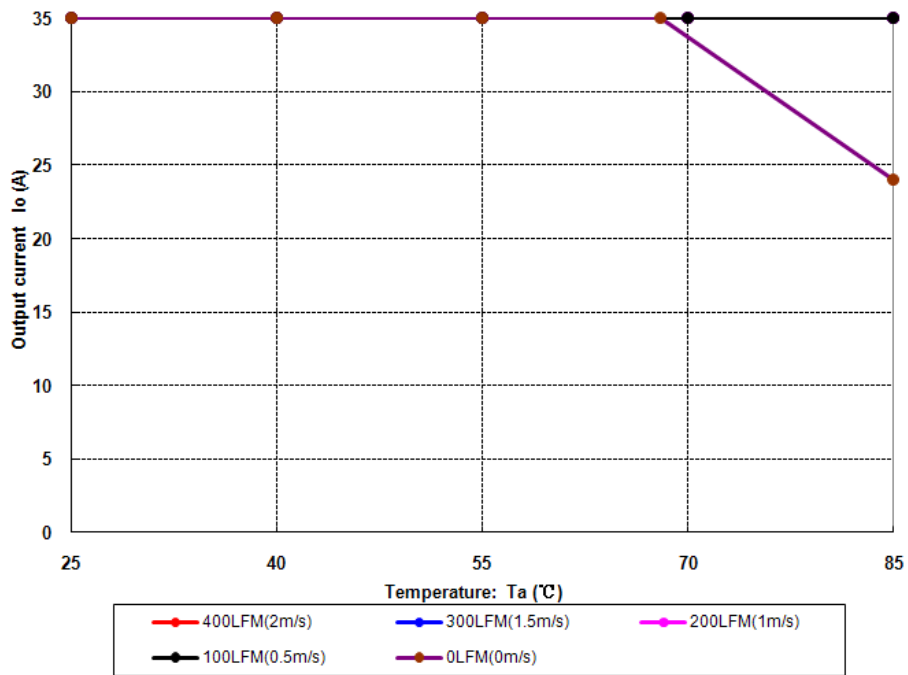
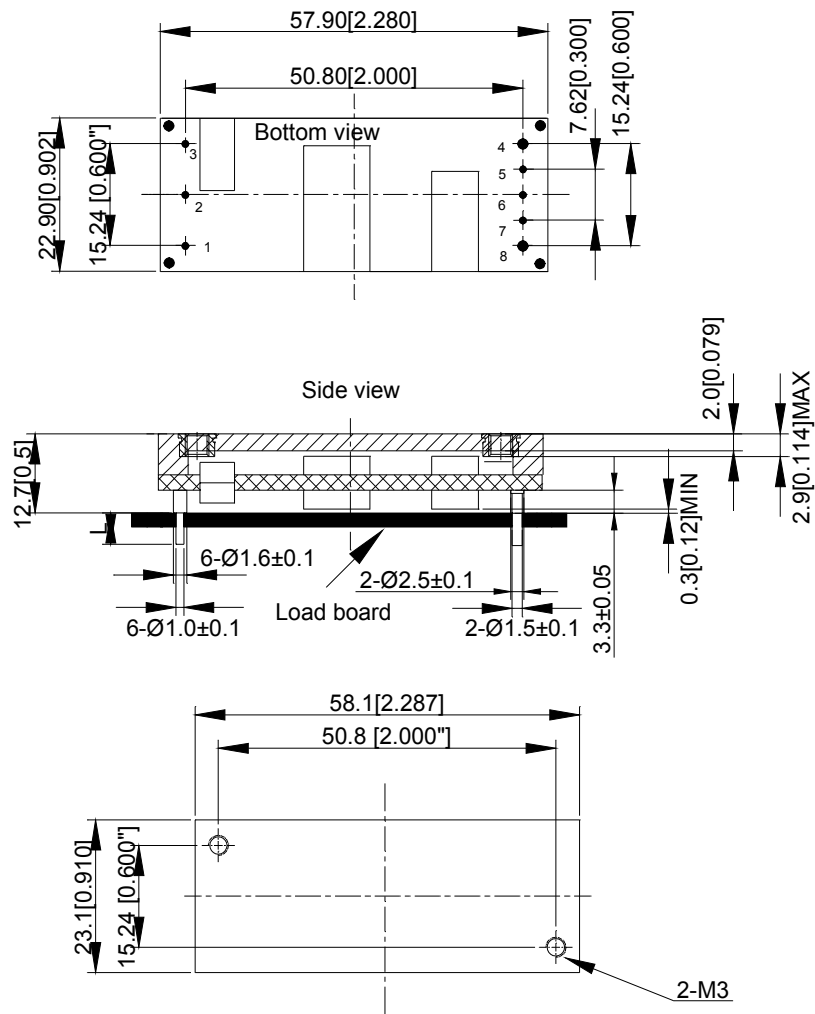


Figure 19 Output power derating, 48V<sub>in</sub>, air flowing across the converter from V<sub>in-</sub> and V<sub>in+</sub>

Mechanical Diagram



Unit: mm[inch]

Bottom view: pin on upside

Tolerance: X.Xmm±0.5mm[X.X in.±0.02in.]

X.XXmm±0.25mm[X.XX in.±0.01in.]

Figure 20 Mechanical diagram

Pin Length Option

Device code suffix	L
-4	4.8mm ± 0.25mm
-6	3.8mm ± 0.25mm
-8	2.8mm ± 0.25mm
None	5.8mm ± 0.25mm

## Pin Designations

Pin No.	Name	Function
1	V <sub>in+</sub>	Positive input voltage
2	Remote ON/OFF	Remote control
3	V <sub>in-</sub>	Negative input voltage
4	V <sub>o-</sub>	Negative output voltage
5	S-	Negative remote sense
6	Trim	Output voltage trim
7	S+	Positive remote sense
8	V <sub>o+</sub>	Positive output voltage

## Soldering

For R6 product, it is intended for standard manual or wave soldering.

When wave soldering is used, the temperature on pins is specified to maximum 260°C for maximum 7s.

When manual soldering is used, the iron temperature should be maintained at 300°C ~ 380°C and applied to the converter pins for less than 10s. Longer exposure can cause internal damage to the converter.

Cleaning of solder joint can be performed with cleaning solvent IPA or similitive.

For R5 product, it is intended for standard manual or wave soldering.

When wave soldering is used, the temperature on pins is specified to maximum 255°C for maximum 7s.

When manual soldering is used, the iron temperature should be maintained at 300°C ~ 380°C and applied to the converter pins for less than 10s. Longer exposure can cause internal damage to the converter.

Cleaning of solder joint can be performed with cleaning solvent IPA or similitive.

## Assembly

The maximum length of the screw driven into the heatsink is 3.3mm.

## Ordering Information

<b>AVO100</b>	<b>-</b>	<b>48</b>	<b>S</b>	<b>2V5</b>		<b>B</b>	<b>-</b>	<b>6</b>	<b>L</b>
①		②	③	④	⑤	⑥		⑦	⑧

①	Model series	AVO: series name; 100: output power 100W
②	Input voltage	48: 36V ~ 75V input range, rated input voltage 48V

③	Output number	S: single output
④	Rated output voltage	2V5: 2.5V output
⑤	Remote ON/OFF logic	Default: negative; P: positive logic
⑥	Baseplate	Default: without baseplate; B: with baseplate
⑦	Pin length	6: 3.8mm
⑧	RoHS status	L: RoHS, R6; Y: RoHS, R5

Model number	Description
AVO100-48S2V5-6L	3.8mm pin length; negative on/off logic; open frame, R6 compliant, see <i>AVO100-48S2V5 TRN</i>
AVO100-48S2V5P-6L	3.8mm pin length; positive on/off logic; open frame, R6 compliant, see <i>AVO100-48S2V5 TRN</i>
AVO100-48S2V5B-6L	3.8mm pin length; negative on/off logic; with baseplate; R6 compliant
AVO100-48S2V5PB-6L	3.8mm pin length; positive on/off logic; with baseplate; R6 compliant
AVO100-48S2V5-6Y	3.8mm pin length; negative on/off logic; open frame, R5 compliant, see <i>AVO100-48S2V5 TRN</i>
AVO100-48S2V5P-6Y	3.8mm pin length; positive on/off logic; open frame, R5 compliant, see <i>AVO100-48S2V5 TRN</i>
AVO100-48S2V5B-6Y	3.8mm pin length; negative on/off logic; with baseplate; R5 compliant
AVO100-48S2V5PB-6Y	3.8mm pin length; positive on/off logic; with baseplate; R5 compliant

## Hazardous Substances Announcement (RoHS Of China)

Parts	Hazardous substances					
	Pb	Hg	Cd	Cr <sup>6+</sup>	PBB	PBDE
AVO100 -48S2V5B	○	○	○	○	○	○
○: Means the content of the hazardous substances in all the average quality materials of the part is within the limits specified in SJ/T-11363-2006 √: Means the content of the hazardous substances in at least one of the average quality materials of the part is outside the limits specified in SJ/T11363-2006						
Emerson Network Power Co., Ltd. has been committed to the design and manufacturing of environment-friendly products. It will reduce and eventually eliminate the hazardous substances in the products through unremitting efforts in research. However, limited by the current technical level, the following parts still contain hazardous substances due to the lack of reliable substitute or mature solution: 1. Solders (including high-temperature solder in parts) contain plumbum. 2. Glass of electric parts contains plumbum. 3. Copper alloy of pins contains plumbum						