

HPS3000-9 FRU Data

OFFSET		DEFINITION (REMARKS)	SPEC VALUE	
		COMMON HEADER, 8 bytes		
000d	00h	Format Version Number (Common Header)	001d	01h
001d	01h	Internal Use Area Offset (In multiples of 8 Bytes)	024d	18h
002d	02h	Chassis Info Area Offset (In multiples of 8 Bytes)	001d	01h
003d	03h	Board Info Area Offset (Not Used)	000d	00h
004d	04h	Product Info Area Offset (In multiples of 8 Bytes)	005d	05h
005d	05h	Multi Record Area Offset (In multiples of 8 Bytes)	015d	0Fh
006d	06h	PAD (reserved – always 00H)	000d	00h
007d	07h	Zero Check Sum (256 – (Sum of bytes 000d to 006d))	210d	D2h
		COMPUTER/CHASSIS INFORMATION AREA, 32 bytes		
008d	08h	Format Version Number (Default value is 1.)	001d	01h
009d	09h	Chassis Info Area Length (Default value is 0.)	000d	00h
010d	0Ah	Chassis Type (Default value is 0.)	000d	00h
011d	0Bh	Chassis Part Number Type/Length 10 byte allocation 0CAH (if used) (Default value is 0.)	000d	00h
012d	0Ch	Chassis Part Number	000d	00h
013d	0Dh	(Default value is 0.)	000d	00h
014d	0Eh		000d	00h
015d	0Fh		000d	00h
016d	10h		000d	00h
017d	11h		000d	00h
018d	12h		000d	00h
019d	13h		000d	00h
020d	14h		000d	00h
021d	15h		000d	00h
022d	16h	Chassis Serial Number Type/Length 15 byte allocation 0CFH (if used) (Default value is 0.)	000d	00h
023d	17h	Chassis Serial Number	000d	00h
024d	18h	(Default value is 0.)	000d	00h
025d	19h		000d	00h
026d	1Ah		000d	00h
027d	1Bh		000d	00h
028d	1Ch		000d	00h
029d	1Dh		000d	00h
030d	1Eh		000d	00h
031d	1Fh		000d	00h
032d	20h		000d	00h
033d	21h		000d	00h
034d	22h		000d	00h
035d	23h		000d	00h
036d	24h		000d	00h
037d	25h		000d	00h
038d	26h	End Tag (0C1H if used) (Default value is 0.)	000d	00h
039d	27h	Zero Check Sum (From 08d to 38d if used)	255d	FFh
		COMPUTER/CHASSIS INFORMATION AREA, 32 bytes		
040d	28h	Format Version Number	001d	01h
041d	29h	Product Info Area Length (In multiple of 8 bytes) 80 Bytes are allocated. 80-Bytes / 8 = 0AH.	010d	0AH

042d	2Ah	Language English = 19H	025d	19h
043d	2Bh	Manufacturer Name Type/Length (0C5H) 7-6: (11)b, 8-Bit ASCII + Latin 1, 5-0: (000101)b, 5-Byte Allocation	197d	C5h
044d	2Ch	Manufacturer Name "E" "N" "P" " "	069d	45h
045d	2Dh		078d	4Eh
046d	2Eh		080d	50h
047d	2Fh		032d	20h
048d	30h		032d	20h
049d	31h	Product Name Type/Length (0CEH) 7-6: (11)b, 8-Bit ASCII + Latin 1, 5-0: (001110)b, 14-Byte Allocation	206d	CEh
050d	32h	Product Name "H" "P" "S" "3" "O" "O" "O" " " "9"	072d	48h
051d	33h		080d	50h
052d	34h		083d	53h
053d	35h		051d	33h
054d	36h		048d	30h
055d	37h		048d	30h
056d	38h		048d	30h
057d	39h		045d	2Dh
058d	3Ah		057d	39h
059d	3Bh		000d	00h
060d	3Ch		000d	00h
061d	3Dh		000d	00h
062d	3Eh		000d	00h
063d	3Fh		000d	00h
064d	40hH	Part/Model Number Type/Length (0CAH) 7-6: (11)b, 8-Bit ASCII + Latin 1, 5-0: (001000)b, 8-Byte Allocation	200d	C8h
065d	41h	Power Supply Spare Kit Number Power Supply Spare Kit Number NOT APPLICABLE	000d	00h
066d	42h		000d	00h
067d	43h		000d	00h
068d	44h		000d	00h
069d	45h		000d	00h
070d	46h		000d	00h
071d	47h		000d	00h
072d	48h		000d	00h
073d	49h	Product Version Number Type/Length (0C2H) 7-6: (11)b, 8-Bit ASCII + Latin 1, 5-0: (000010)b, 2-Byte Allocation	194d	C2h
074d	4Ah	Product Version Number / Auto Rev *SHOULD TRACK MODEL REVISION on IPS Sec. 1.2	XXXd	XXh
075d	4Bh		XXXd	XXh
076d	4Ch	Product Serial Number Type/Length (0CDH) *PRODUCT SERIAL NUMBER IS BASED ON ASTEC SERIAL NUMBER FORMAT P/N: 417-00201000 7-6: (11)b, 8-Bit ASCII + Latin 1, 5-0: (001101)b, 13-Byte Allocation	205d	CDh

077d	4Dh	Product Serial Number: MODEL ID “H” “O” “8” “1”	072d	48h
078d	4Eh		048d	30h
079d	4Fh		056d	38h
080d	50h		049d	31h
081d	51h	Product Serial Number: MANUFACTURING YEAR AND WEEK CODE *REFER TO 417-00201000 FOR DETAILS	XXXd	XXh
082d	52h		XXXd	XXh
083d	53h	Product Serial Number: UNIQUE SERIAL NUMBER *REFER TO 417-00201000 FOR DETAILS	XXXd	XXh
084d	54h		XXXd	XXh
085d	55h		XXXd	XXh
086d	56h		XXXd	XXh
087d	57h	Product Serial Number: MODEL REVISION *SHOULD TRACK MODEL REVISION on IPS Sec. 1.2	XXXd	XXh
088d	58h		XXXd	XXh
089d	59h	Product Serial Number: MANUFACTURING LOCATION *REFER TO 417-00201000 FOR DETAILS	080d	50h
090d	5Ah		200d	C8h
091d	5Bh	ASSET TAG 7-6: (11)b, 8-Bit ASCII + Latin 1, 5-0: (001000)b, 8-Byte Allocation NO ASSET TAG	000d	00h
092d	5Ch		000d	00h
093d	5Dh		000d	00h
094d	5Eh		000d	00h
095d	5Fh		000d	00h
096d	60h		000d	00
097d	61h		000d	00h
098d	62h		000d	00h
099d	63h	FRU File ID 7-6: (11)b, 8-Bit ASCII + Latin 1, 5-0: (010001)b, 17-Byte Allocation	209d	D1h
100d	64h	“Should track latest EEPROM Revision on IPS Sec. 1.2” NOT APPLICABLE	000d	00h
101d	65h		000d	00h
102d	66h		000d	00h
103d	67h		000d	00h
104d	68h		000d	00h
105d	69h		000d	00h
106d	6Ah		000d	00h
107d	6Bh		000d	00h
108d	6Ch		000d	00h
109d	6Dh		000d	00h
110d	6Eh		000d	00h
111d	6Fh		000d	00h
112d	70h		000d	00h
113d	71h		000d	00h
114d	72h	000d	00h	

115d	73h		000d	00h
116d	74h		000d	00h
117d	75h	End of Fields Marker	193d	C1h
118d	76h	RESERVED	000d	00h
119d	77h	Zero Checksum From 040d to 118d	XXXd	XXh

MULTI RECORD AREA : Power Supply Information 72 Bytes				
Power Supply Record Header				
120d	78h	Record Type ID (0x00 = Power Supply Information)	000d	00h
121d	79h	7: (0)b, End of List 6-4: (000)b, Reserved 3-0: (0010)b, Record Format Version	002d	02h
122d	7Ah	Record Length: 24 Bytes	024d	18h
123d	7Bh	Record Checksum (Zero Checksum From 125d To 148d)	100d	64h
124d	7Ch	Header Checksum (Zero Checksum From 120d To 123d)	130d	82h
Power Supply Record				
125d	7Dh	Overall Capacity (Watts) 15-12: (0000)b, Reserved 11-0: (11111010000)b, 3000W Stored with LSB first then MSB.	184d	B8h
126d	7Eh		011d	0Bh
127d	7Fh	Peak VA (Watts) 15-12: (0000)b, Reserved 11-0: No peak VA rating Stored with LSB first then MSB.	000d	00h
128d	80h		000d	00h
129d	81h	Inrush Current (Amps) 40Amps	040d	28h
130d	82h	Inrush Interval (ms) 0ms	000d	00h
131d	83h	Low End Input Voltage Range 1	000d	00h
132d	84h		000d	00h
133d	85h	High End Input Voltage Range 1	000d	00h
134d	86h		000d	00h
135d	87h	Low End Input Voltage Range 2 180V = 18000 (x10mV) Stored with LSB first then MSB.	080d	50h
136d	88h		070d	46h
137d	89h	High End Input Voltage Range 2 264V = 26400 (x10mV) Stored with LSB first then MSB.	032d	20h
138d	8Ah		103d	67h
139d	8Bh	Low End Input Frequency Range , 47Hz	047d	2Fh
140d	8Ch	High End Input Frequency Range , 63Hz	063d	3Fh
141d	8Dh	A/C Dropout Tolerance in ms , 12ms	012d	0Ch
142d	8Eh	Binary Flags 7-5: (000)b, Reserved 4: (1)b, Tachometer Pulses per Rotation / Predictive Fail Polarity (2 Pulses Per Rotation = 1; 1 Pulse Per Rotation = 0) OR (Signal Asserted(1) Indicates Failure = 0, Signal Deasserted(0) Indicates Failure = 1) 3: (1)b, Hot Swap / Redundancy Support 2: (0)b, AutoSwitch Support 1: (1)b, Power Factor Correction Support 0: (0)b, Predictive Fail Support	026d	1Ah

143d	8Fh	Peak Wattage Capacity and Holdup Time		
144d	90h	15-12: (0000)b, Hold Up Time in Seconds = 00H (Not Specified)	000d	00h
		11-0: (00000000000)b, Peak Capacity in Watts = 00H (Not Specified)	000d	00h
145d	91h	Combined Wattage	000d	00h
146d	92h	NOT APPLICABLE	000d	00h
147d	93h		000d	00h
148d	94h	Predictive Fail Tachometer Lower Threshold, Not applicable	000d	00h

		48V DC Output Record Header		
149d	95h	Record Type ID (0x01 = DC Output)	001d	01h
150d	96h	End Of List/Record Format Version Number	002d	02h
		7: (0)b, End of List		
		6-4: (000)b, Reserved		
		3-0: (0010)b, Record Format Version		
151d	97h	Record Length: 13 Bytes	013d	0Dh
152d	98h	Record Checksum (Zero Checksum From 154d To 166d)	194d	C2h
153d	99h	Header Checksum (Zero Checksum From 149d To 152d)	46d	2Eh
		+48V DC Output Record		
154d	9Ah	+48V Output Information	001d	01h
		7: (0)b, Standby		
		6-4: (000)b, Reserved		
		3-0: (0001)b, Output Number 1		
155d	9Bh	Nominal Voltage	192d	C0h
156d	9Ch	48V = 4800 (x10mV)	018d	12h
		Stored with LSB first then MSB.		
157d	9Dh	Maximum Negative Voltage Deviation	224d	E0h
158d	9Eh	43.2V = 4320 (x10mV)	016d	10h
		Stored with LSB first then MSB.		
159d	9Fh	Maximum Positive Voltage Deviation	160d	A0h
160d	A0h	52.8V = 5280x(10mV)	020d	14h
		Stored with LSB first then MSB.		
161d	A1h	Ripple And Noise pk -pk 10Hz To 30MHz (mV)	224d	E0h
162d	A2h	480mV	001d	01h
		Stored with LSB first then MSB.		
163d	A3h	Minimum Current Draw (10mA)	100d	64h
164d	A4h	1A = 100 (x10mA)	000d	00h
		Stored with LSB first then MSB.		
165d	A5h	Maximum Current Draw (10mA)	106d	6Ah
166d	A6h	62.50A = 6250 (x10mA)	024d	18h
		Stored with LSB first then MSB.		

		5VSB DC Output Record Header		
167d	A7h	Record Type ID (0x01 = DC Output)	001d	01h
168d	A8h	End Of List/Record Format Version Number	130d	82h
		7: (1)b, End of List		
		6-4: (000)b, Reserved		
		3-0: (0010)b, Record Format Version		
169d	A9h	Record Length: 20 Bytes	020d	14h
170d	AAh	Record Checksum (Zero Checksum From 172d To 191d)	219d	DBh
171d	Abh	Header Checksum (Zero Checksum From 167d To 170d)	142d	8Eh
		5VSB DC Output Record		

172d	ACh	5VSB Output Information 7: (1)b, Standby (Bit = 1 to indicate standby output) 6-4: (000)b, Reserved 3-0: (0010)b, Output Number 2 = 010b	130d	82h
173d	ADh	Nominal Voltage 5V = 500 (x10mV) Stored with LSB first then MSB.	244d	F4h
174d	A Eh		001d	01h
175d	AFh	Maximum Negative Voltage Deviation 4.8V = 480 (x10mV) Stored with LSB first then MSB.	224d	E0h
176d	B0h		001d	01h
177d	B1h	Maximum Positive Voltage Deviation 5.2V = 520 (x10mV) Stored with LSB first then MSB.	008d	08h
178d	B2h		002d	02h
179d	B3h	Ripple And Noise pk -pk 10Hz To 30MHz (mV) 100mV Stored with LSB first then MSB.	100d	64h
180d	B4h		000d	00h
181d	B5h	Minimum Current Draw (10mA) 0.5A = 50(x 10mA) Stored with LSB first then MSB.	050d	32h
182d	B6h		000d	00h
183d	B7h	Maximum Current Draw (10mA) 3.00A = 300(x 10mA) Stored with LSB first then MSB.	044d	2Ch
184d	B8h		001d	01h
185d	B9h	Reserved	000d	00h
186d	BAh	Reserved	000d	00h
187d	BBh	Reserved	000d	00h
188d	BCh	Reserved	000d	00h
189d	BDh	Reserved	000d	00h
190d	BEh	Reserved	000d	00h
191d	BFh	Reserved	000d	00h

		INTERNAL USE AREA, 64 bytes		
192d	0C0h	Format Version Number 7:4 -reserved, write as 0000b 3:0 -format version number = 1h for this specification.	001d	01h
193d	C1h		000d	00h
194d	C2h		000d	00h
195d	C3h		000d	00h
196d	C4h		000d	00h
197d	C5h		000d	00h
198d	C6h		000d	00h
199d	C7h		000d	00h
200d	C8h		000d	00h
201d	C9h		000d	00h
202d	CAh		000d	00h
203d	CBh		000d	00h
204d	CCh		000d	00h
205d	CDh		000d	00h
206d	CEh		000d	00h
207d	CFh		000d	00h
208d	D0h		000d	00h
209d	D1h		000d	00h
210d	D2h		000d	00h
211d	D3h		000d	00h

212d	D4h		000d	00h
213d	D5h		000d	00h
214d	D6h		000d	00h
215d	D7h		000d	00h
216d	D8h		000d	00h
217d	D9h		000d	00h
218d	DAh		000d	00h
219d	DBh		000d	00h
220d	DCh		000d	00h
221d	DDh		000d	00h
222d	DEh		000d	00h
223d	DFh		000d	00h
224d	E0h		000d	00h
225d	E1h		000d	00h
226d	E2h		000d	00h
227d	E3h		000d	00h
228d	E4h		000d	00h
229d	E5h		000d	00h
230d	E6h		000d	00h
231d	E7h		000d	00h
232d	E8h		000d	00h
233d	E9h		000d	00h
234d	EAh		000d	00h
235d	EBh		000d	00h
236d	ECh		000d	00h
237d	EDh		000d	00h
238d	EEh		000d	00h
239d	EFh		000d	00h
240d	F0h		000d	00h
241d	F1h		000d	00h
242d	F2h		000d	00h
243d	F3h		000d	00h
244d	F4h		000d	00h
245d	F5h		000d	00h
246d	F6h		000d	00h
247d	F7h		000d	00h
248d	F8h		000d	00h
249d	F9h		000d	00h
250d	FAh		000d	00h
251d	FBh		000d	00h
252d	FCh		000d	00h
253d	FDh		000d	00h
254d	FEh		000d	00h
255d	FFh	Zero Checksum From 192d to 254d if used	255d	FFh